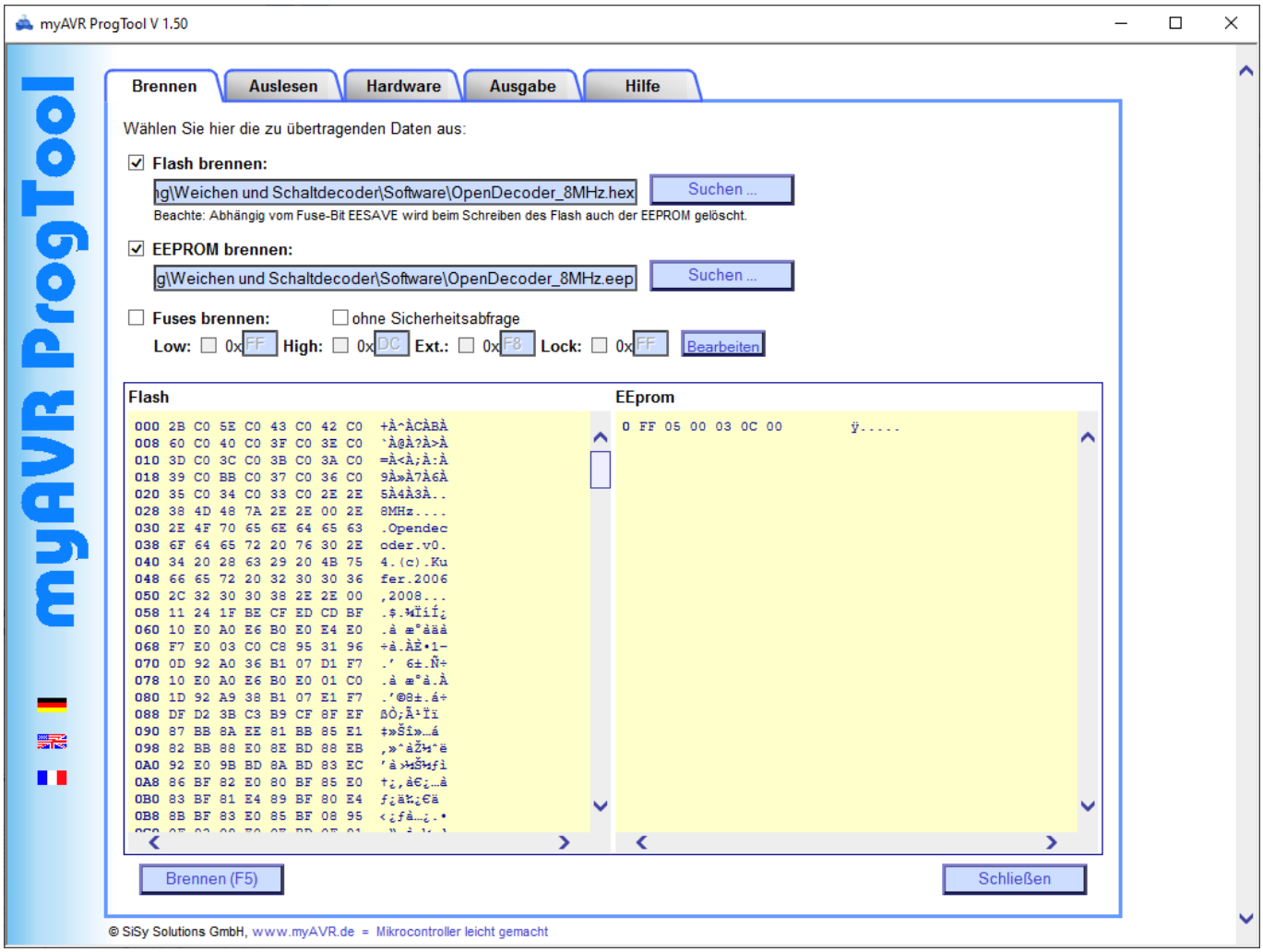
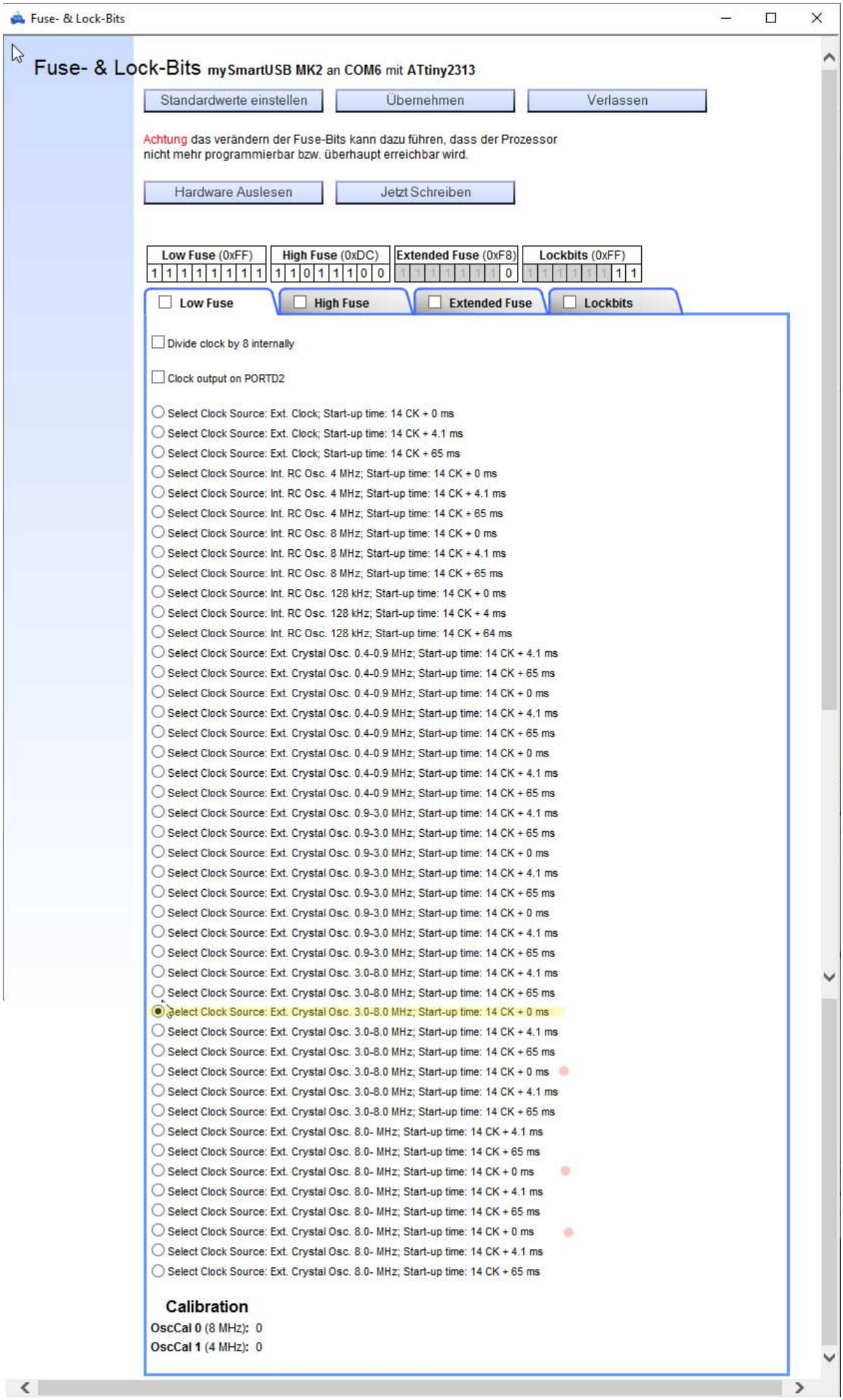
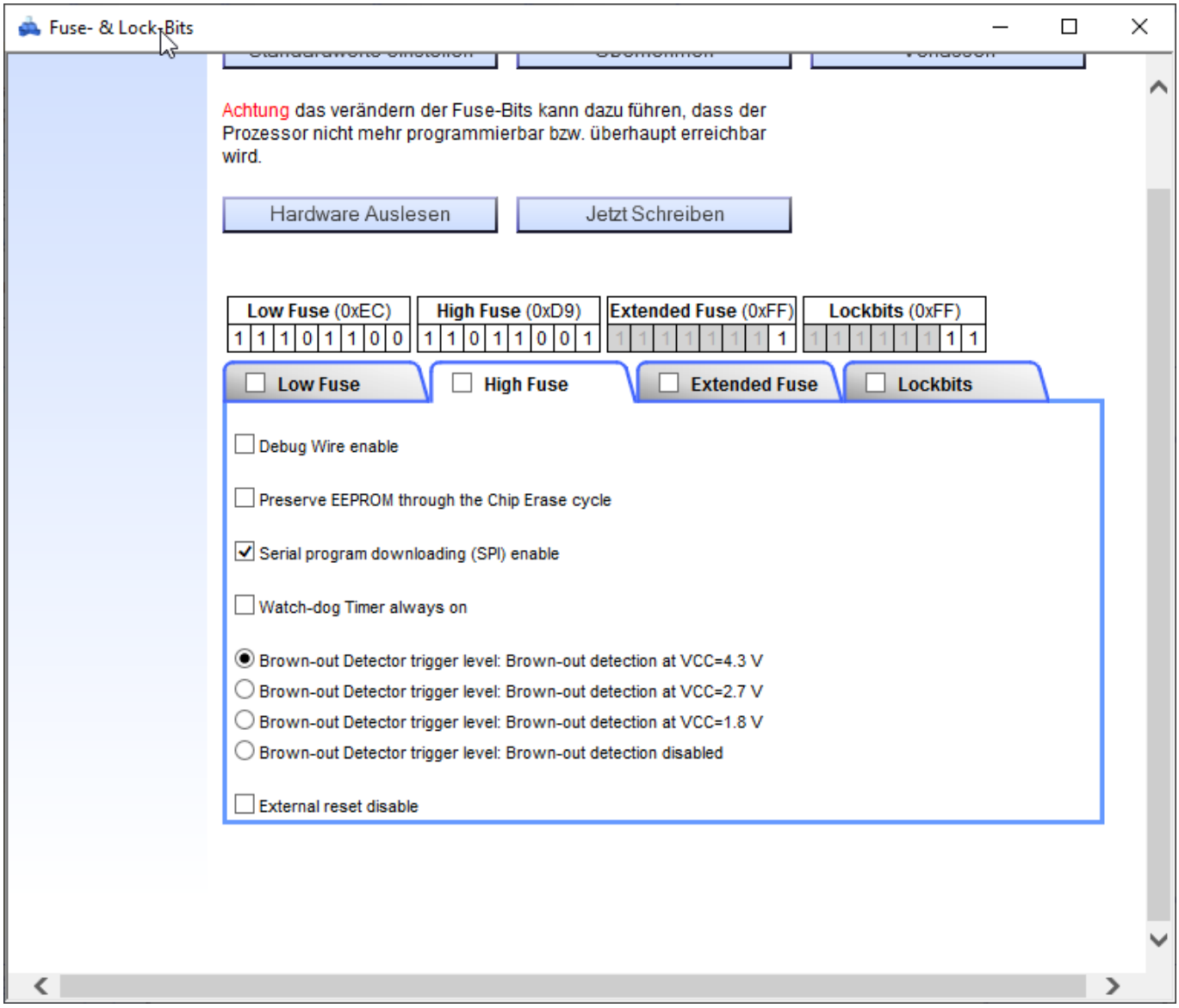
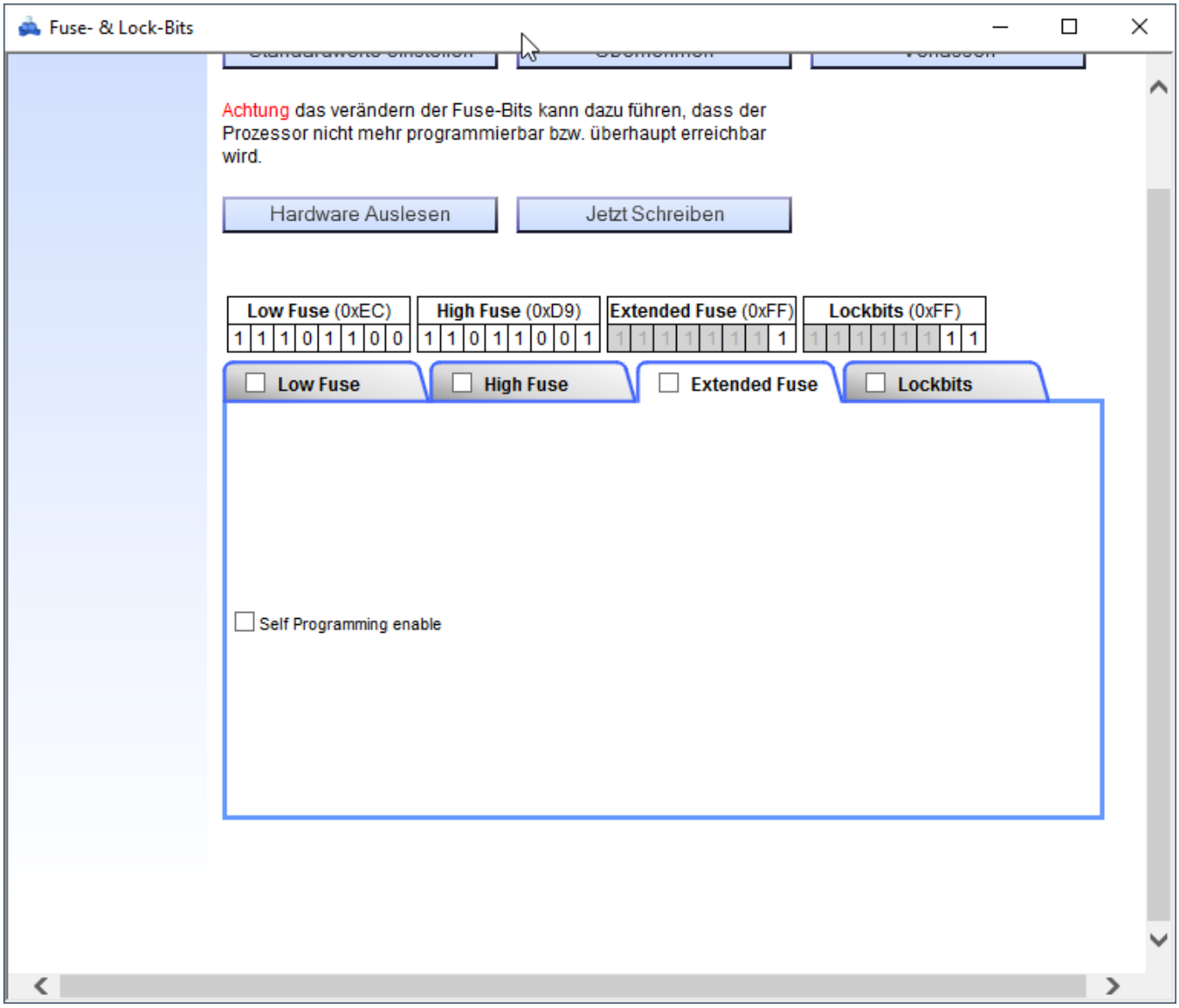
Einstellung der FuseBits mit MyAVR für Modellbahn Decoder

Als Träger nimmt man ATtiny2313-20PU




Fuse- & Lock-Bits
Fuse- & Lock-Bits mySmartUSB MK2 an COM6 mit ATtiny2313
Standardwerte einstellen
Übernehmen
Verlassen
Achtung das verändern der Fuse-Bits kann dazu führen, dass der Prozessor nicht mehr programmierbar bzw. überhaupt erreichbar wird.
Hardware Auslesen
Jetzt Schreiben
Low Fuse (0xFF) 1 1 1 1 1 1 1 1
High Fuse (0xDC) 1 1 0 1 1 1 0 0
Extended Fuse (0xF8) 1 1 1 1 1 1 1 0
Lockbits (0xFF) 1 1 1 1 1 1 1 1
Low Fuse
High Fuse
Extended Fuse
Lockbits
Divide clock by 8 internally
Clock output on PORTD2
Select Clock Source: Ext. Clock; Start-up time: 14 CK + 0 ms
Select Clock Source: Ext. Clock; Start-up time: 14 CK + 4.1 ms
Select Clock Source: Ext. Clock; Start-up time: 14 CK + 65 ms
Select Clock Source: Int. RC Osc. 4 MHz; Start-up time: 14 CK + 0 ms
Select Clock Source: Int. RC Osc. 4 MHz; Start-up time: 14 CK + 4.1 ms
Select Clock Source: Int. RC Osc. 4 MHz; Start-up time: 14 CK + 65 ms
Select Clock Source: Int. RC Osc. 8 MHz; Start-up time: 14 CK + 0 ms
Select Clock Source: Int. RC Osc. 8 MHz; Start-up time: 14 CK + 4.1 ms
Select Clock Source: Int. RC Osc. 8 MHz; Start-up time: 14 CK + 65 ms
Select Clock Source: Int. RC Osc. 128 kHz; Start-up time: 14 CK + 0 ms
Select Clock Source: Int. RC Osc. 128 kHz; Start-up time: 14 CK + 4 ms
Select Clock Source: Int. RC Osc. 128 kHz; Start-up time: 14 CK + 64 ms
Select Clock Source: Ext. Crystal Osc. 0.4-0.9 MHz; Start-up time: 14 CK + 4.1 ms
Select Clock Source: Ext. Crystal Osc. 0.4-0.9 MHz; Start-up time: 14 CK + 65 ms
Select Clock Source: Ext. Crystal Osc. 0.4-0.9 MHz; Start-up time: 14 CK + 0 ms
Select Clock Source: Ext. Crystal Osc. 0.4-0.9 MHz; Start-up time: 14 CK + 4.1 ms
Select Clock Source: Ext. Crystal Osc. 0.4-0.9 MHz; Start-up time: 14 CK + 65 ms
Select Clock Source: Ext. Crystal Osc. 0.4-0.9 MHz; Start-up time: 14 CK + 0 ms
Select Clock Source: Ext. Crystal Osc. 0.4-0.9 MHz; Start-up time: 14 CK + 4.1 ms
Select Clock Source: Ext. Crystal Osc. 0.4-0.9 MHz; Start-up time: 14 CK + 65 ms
Select Clock Source: Ext. Crystal Osc. 0.9-3.0 MHz; Start-up time: 14 CK + 4.1 ms
Select Clock Source: Ext. Crystal Osc. 0.9-3.0 MHz; Start-up time: 14 CK + 65 ms
Select Clock Source: Ext. Crystal Osc. 0.9-3.0 MHz; Start-up time: 14 CK + 0 ms
Select Clock Source: Ext. Crystal Osc. 0.9-3.0 MHz; Start-up time: 14 CK + 4.1 ms
Select Clock Source: Ext. Crystal Osc. 0.9-3.0 MHz; Start-up time: 14 CK + 65 ms
Select Clock Source: Ext. Crystal Osc. 0.9-3.0 MHz; Start-up time: 14 CK + 0 ms
Select Clock Source: Ext. Crystal Osc. 0.9-3.0 MHz; Start-up time: 14 CK + 4.1 ms
Select Clock Source: Ext. Crystal Osc. 0.9-3.0 MHz; Start-up time: 14 CK + 65 ms
Select Clock Source: Ext. Crystal Osc. 3.0-8.0 MHz; Start-up time: 14 CK + 4.1 ms
Select Clock Source: Ext. Crystal Osc. 3.0-8.0 MHz; Start-up time: 14 CK + 65 ms
Select Clock Source: Ext. Crystal Osc. 3.0-8.0 MHz; Start-up time: 14 CK + 0 ms
Select Clock Source: Ext. Crystal Osc. 3.0-8.0 MHz; Start-up time: 14 CK + 4.1 ms
Select Clock Source: Ext. Crystal Osc. 3.0-8.0 MHz; Start-up time: 14 CK + 65 ms
Select Clock Source: Ext. Crystal Osc. 3.0-8.0 MHz; Start-up time: 14 CK + 0 ms
Select Clock Source: Ext. Crystal Osc. 3.0-8.0 MHz; Start-up time: 14 CK + 4.1 ms
Select Clock Source: Ext. Crystal Osc. 3.0-8.0 MHz; Start-up time: 14 CK + 65 ms
Select Clock Source: Ext. Crystal Osc. 8.0- MHz; Start-up time: 14 CK + 4.1 ms
Select Clock Source: Ext. Crystal Osc. 8.0- MHz; Start-up time: 14 CK + 65 ms
Select Clock Source: Ext. Crystal Osc. 8.0- MHz; Start-up time: 14 CK + 0 ms
Select Clock Source: Ext. Crystal Osc. 8.0- MHz; Start-up time: 14 CK + 4.1 ms
Select Clock Source: Ext. Crystal Osc. 8.0- MHz; Start-up time: 14 CK + 65 ms
Select Clock Source: Ext. Crystal Osc. 8.0- MHz; Start-up time: 14 CK + 0 ms
Select Clock Source: Ext. Crystal Osc. 8.0- MHz; Start-up time: 14 CK + 4.1 ms
Select Clock Source: Ext. Crystal Osc. 8.0- MHz; Start-up time: 14 CK + 65 ms
Calibration
OscCal 0 (8 MHz): 0
OscCal 1 (4 MHz): 0


Fuse- & Lock-Bits
Achtung das verändern der Fuse-Bits kann dazu führen, dass der Prozessor nicht mehr programmierbar bzw. überhaupt erreichbar wird.
Hardware Auslesen
Jetzt Schreiben
Low Fuse (0xEC)
1 1 1 0 1 1 0 0
High Fuse (0xD9)
1 1 0 1 1 0 0 1
Extended Fuse (0xFF)
1 1 1 1 1 1 1 1
Lockbits (0xFF)
1 1 1 1 1 1 1 1
Low Fuse
High Fuse
Extended Fuse
Lockbits
Debug Wire enable
Preserve EEPROM through the Chip Erase cycle
Serial program downloading (SPI) enable
Watch-dog Timer always on
Brown-out Detector trigger level: Brown-out detection at VCC=4.3 V
Brown-out Detector trigger level: Brown-out detection at VCC=2.7 V
Brown-out Detector trigger level: Brown-out detection at VCC=1.8 V
Brown-out Detector trigger level: Brown-out detection disabled
External reset disable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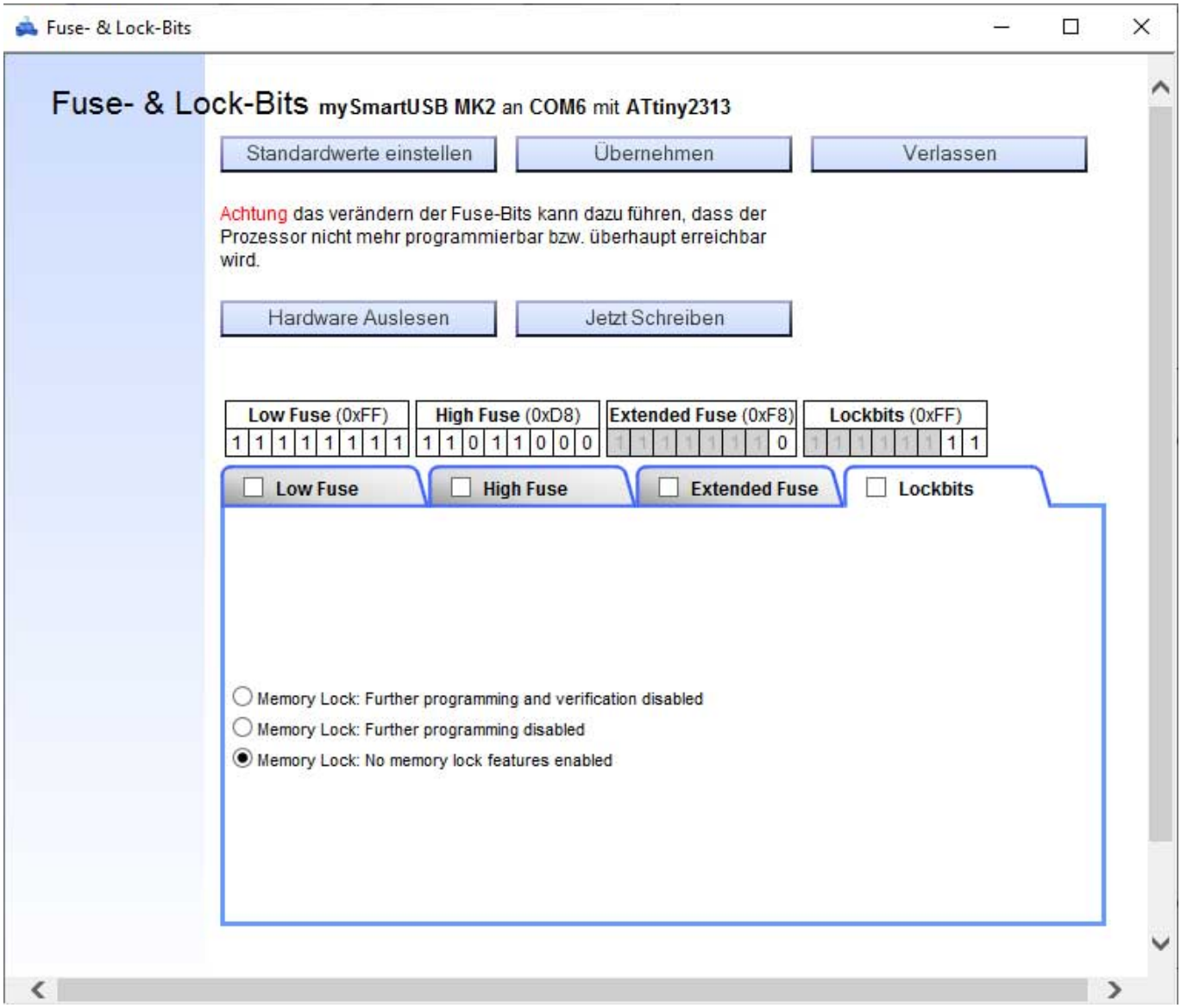
Hardware Auslesen
Jetzt Schreiben
Low Fuse (0xEC)
1 1 1 0 1 1 0 0
High Fuse (0xD9)
1 1 0 1 1 0 0 1
Extended Fuse (0xFF)
1 1 1 1 1 1 1 1
Lockbits (0xFF)
1 1 1 1 1 1 1 1
Low Fuse
High Fuse
Extended Fuse
Lockbits
Self Programming enable



FuseBits, MyAVR, Modellbahn, Decoder